

## ABSTRACT

A method and apparatus is disclosed for implementing an integrated video  
5 card and smart card reader. A single processor is used to perform both video  
and smart card reader functions. The processor simulates a PCI-to-PCMCIA  
detection logic scheme. An operating system, such as Windows, detects both  
a video card and a PCI-to-PCMCIA bridge. A smart card reader is attached to  
the integrated video card and smart card reader.

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